CLAIMS

What is claimed is:
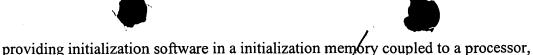
1 1.   A computer system comprising:

2   initialization memory containing initialization code,

3   a processor coupled to said initialization memory for executing said initialization code,

4   a static random access memory coupled to said processor for use in executing said

5 initialization code.

1 2.   The apparatus of Claim 1 wherein said static random access memory is assigned

2 addresses overlaying a portion of the addresses assigned to said initialization memory.

1 3.   The apparatus of Claim 2 further including logic for selecting the initialization memory

2 when the processor needs to read initialization code and for selecting the static random access

3 memory when the processor needs to read or write to random access memory.

1 4.   The apparatus of Claim 1 further including dynamic random access memory coupled to

2 said processor, wherein said initialization code is for initializing said dynamic random access

3 memory.

1 5.   The apparatus of Claim 4 wherein said processor uses primarily only said dynamic

2 random access memory when executing application code.

1 6.   A method for operating a computer system comprising;

2   providing initialization software in a initialization memory coupled to a processor,

3   providing static random access memory coupled to the processor,

4   executing the initialization code in the processor while using the static random access

5 memory to store and retrieve variables needed by the code.

7.   The method of Claim 6 wherein said computer system includes dynamic random access

2 memory and said initialization code is for initializing said dynamic random access memory.

1 8.   The method of Claim 7 further including using primarily only said dynamic random

2 access memory when executing application code in said processor.

1 9.   A computer system comprising:

2   dynamic random access memory,

3   initialization memory containing initialization code for initializing the dynamic random

4 access memory at system startup, and

5   a static random access memory functional at system startup.

1 10.   The system of Claim 9, further including:

2   a processor coupled to said initialization memory for executing said initialization code

3 upon system startup and coupled to said static random access memory for use in executing said

4 code.

1   11.   The system of Claim 10 wherein said processor is coupled to said static random access

2   memory after system startup for use in executing system code other than said initialization code.

1   12.   The system of Claim 9 wherein said static random access memory is assigned addresses

2   overlaying a portion of the address space assigned to said initialization memory.

1   13.   The system of Claim 12 further including means for selecting said static random access

2   memory when said processor is executing said initialization code.

1   14.   A computer system comprising:

2   dynamic random access memory,

3   initialization memory containing initialization code for initializing the dynamic random

4   access memory at system startup,

5   a processor coupled to said initialization memory for executing said initialization code,

6   a static random access memory coupled to said processor for use in executing said

7   initialization code, said static random access memory connected to and powered by a system

8   power supply which remains active whenever AC power is supplied to the computer system.